App No.: Not Yet Assigned Inventor: Terufumi ISHIDA

Docket No.: 299002054910

Title: CHARGING CIRCUIT AND SEMICONDUCTOR MEMORY DEVICE USING THE SAME

01R00750

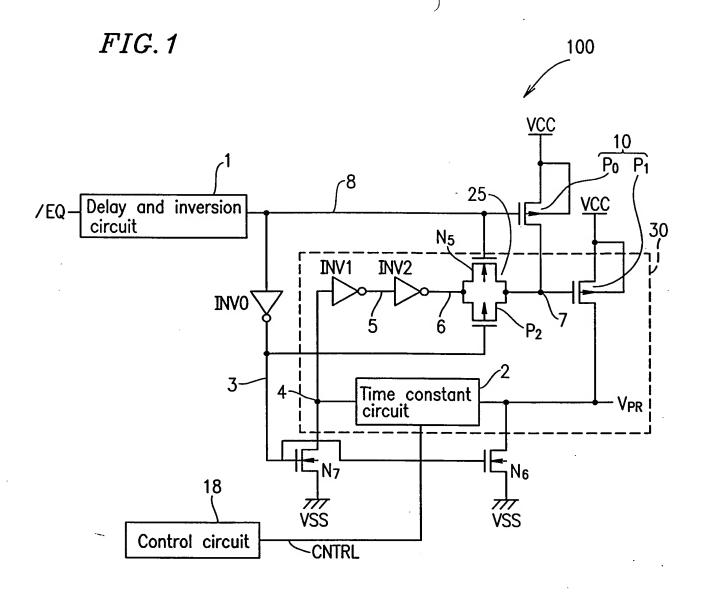
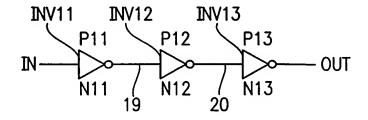


FIG.2



LP<sub>11</sub>, LN<sub>12</sub>: Long

LN<sub>11</sub>, LP<sub>12</sub>, LP<sub>13</sub>, LN<sub>13</sub>: Short

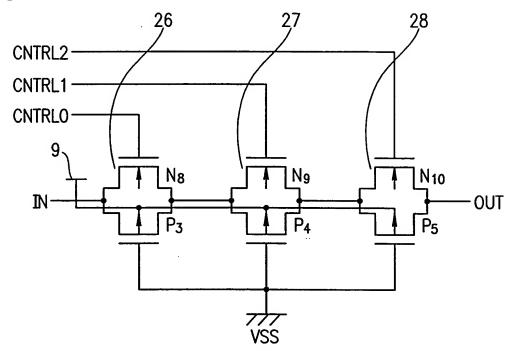
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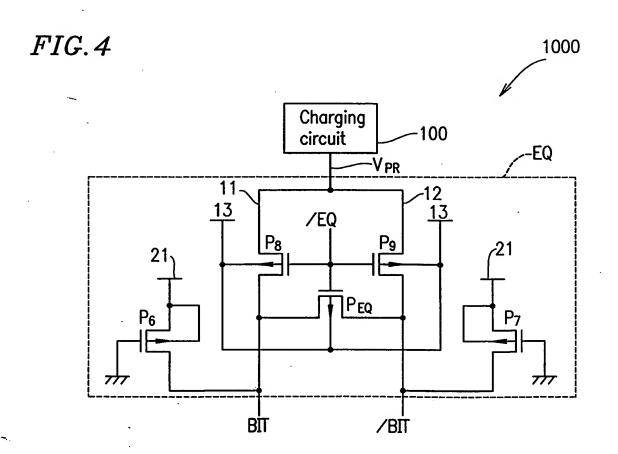
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FIG.3





Title: CHARGING CIRCUIT AND SEMICONDUCTOR MEMORY

DEVICE USING THE SAME

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FIG.5

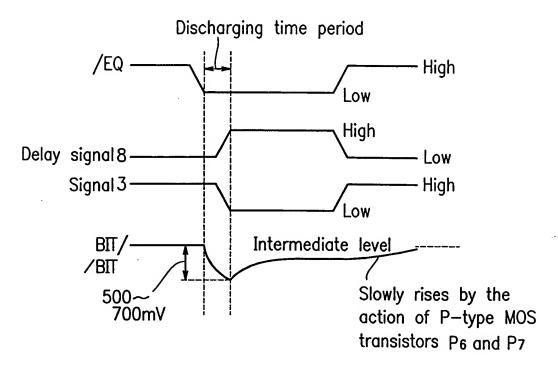
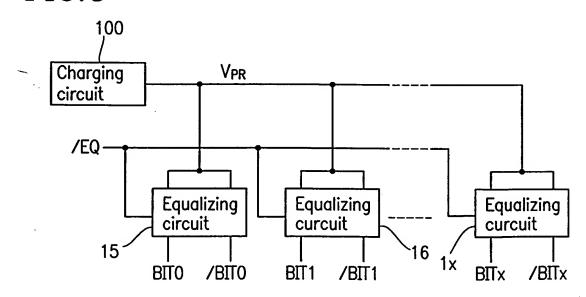


FIG.6



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FIG.7

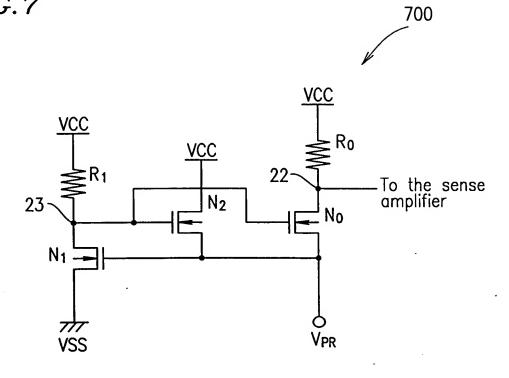


FIG.8

